

Equipment problems

Notes on back

revolution

May 04 Sat

Revolution Hall

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Kit No:	Director:	Cam Op:	Audio op:	File count: _____
<b>Pre-Day check:</b> <input type="checkbox"/> Replace Batteries in mics Sound levels: <input type="checkbox"/> L-PA <input type="checkbox"/> R-cam mic <input type="checkbox"/> System date and time.		<b>Post production:</b> <input type="checkbox"/> Files synked Edit by: <input type="checkbox"/> Set Scanned Review by: <input type="checkbox"/> Edit - set done <input type="checkbox"/> Review - done		

Sound check Title:  Welcome  
 Presenter(s):

Start: HH\_MM\_SS

09:00 (9:00 am)

Length: 0:10:0

End: HH\_MM\_SS Released:  yes  no.

misc.

09:10 (9:10 am) Presenter email:

id: 14798 conf: 0

Presenter initials for intent to release: \_\_\_\_\_

Edit  Review

Sound check Title:  FOSSi Foundation Update  
 Presenter(s):

Start: HH\_MM\_SS

09:10 (9:10 am)

Length: 0:20:0

End: HH\_MM\_SS Released:  yes  no.

misc.

09:30 (9:30 am) Presenter email:

id: 14799 conf: 1

Presenter initials for intent to release: \_\_\_\_\_

Edit  Review

Sound check Title:  Diagrams and system visualisation in chip design  
 Presenter(s): Aliaksei Chapyzenka

Start: HH\_MM\_SS

09:30 (9:30 am)

Length: 0:30:0

End: HH\_MM\_SS Released:  yes  no.

misc.

10:00 (10:00 am) Presenter email:

id: 14800 conf: 2

Presenter initials for intent to release: \_\_\_\_\_

Edit  Review

Sound check

Unscheduled

Start: \_\_\_\_\_

Lighting Talks

Keynote

misc.

Added to Veyepar

End: \_\_\_\_\_

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Sound check

Title:  Netlistsvg: How to Draw a Better Schematic than Graphviz

Presenter(s): Neil Turley

Start: HH\_MM\_SS

10:00 (10:00 am)

Length: 0:30:0

End: HH\_MM\_SS

10:30 (10:30 am)

id: 14801 conf: 3

Presenter email:

misc.

Edit

Review

Sound check

Title:  Lessons learned customising the Rocket RISC-V core

Presenter(s): Julius Baxter

Start: HH\_MM\_SS

10:50 (10:50 am)

Length: 0:20:0

End: HH\_MM\_SS

11:10 (11:10 am)

id: 14802 conf: 4

Presenter email:

misc.

Edit

Review

Sound check

Title:  Higher-Order Hardware Design with Chisel 3

Presenter(s): Jack Koenig

Start: HH\_MM\_SS

11:10 (11:10 am)

Length: 0:30:0

End: HH\_MM\_SS

11:40 (11:40 am)

id: 14803 conf: 5

Presenter email:

misc.

Edit

Review

Sound check

Title:  The fusion of high-level synthesis with event-oriented hardware description (myhdl)

Presenter(s): Christopher Felton

Start: HH\_MM\_SS

11:40 (11:40 am)

Length: 0:30:0

End: HH\_MM\_SS

12:10 (12:10 pm)

id: 14804 conf: 6

Presenter email:

misc.

Edit

Review

Sound check

Unscheduled

Start: \_\_\_\_\_

Lighting Talks

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Added to Veyepar

End: \_\_\_\_\_

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Sound check

Title:  JuxtaPiton: The First Open-Source, Heterogeneous-ISA Processor

Presenter(s): Katie Lim

Start: HH\_MM\_SS

13:30 (1:30 pm)

Length: 0:20:0

End: HH\_MM\_SS

13:50 (1:50 pm)

id: 14805 conf: 7

Presenter email:

misc.

Edit

Review

Sound check

Title:  OpenPiton+Ariane: Making Ariane Multicore with OpenPiton's P-Mesh

Presenter(s): Jonathan Balkind

Start: HH\_MM\_SS

13:50 (1:50 pm)

Length: 0:20:0

End: HH\_MM\_SS

14:10 (2:10 pm)

id: 14806 conf: 8

Released:  yes  no.

Presenter email:

misc.

Presenter initials for intent to release: \_\_\_\_\_

Edit

Review

Sound check

Title:  Lessons Learned from Open-Sourcing NVDLA

Presenter(s): Joshua Wise

Start: HH\_MM\_SS

14:10 (2:10 pm)

Length: 0:20:0

End: HH\_MM\_SS

14:30 (2:30 pm)

id: 14807 conf: 9

Released:  yes  no.

Presenter email:

misc.

Presenter initials for intent to release: \_\_\_\_\_

Edit

Review

Sound check

Title:  DVKit: An Integrated Development Environment for Design and Verification Engineers

Presenter(s): Matthew Ballance

Start: HH\_MM\_SS

14:30 (2:30 pm)

Length: 0:20:0

End: HH\_MM\_SS

14:50 (2:50 pm)

id: 14808 conf: 10

Presenter email:

misc.

Edit

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Sound check

Unscheduled

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Sound check

Title:  Live Graph infrastructure for Synthesis and Simulation

Presenter(s): Jose Renau

Start: HH\_MM\_SS

15:30 (3:30 pm)

Length: 0:30:0

End: HH\_MM\_SS

16:00 (4:00 pm)

id: 14809 conf: 11

Presenter email:

misc.

Edit

Review

Sound check

Title:  The Berkeley-Out-of-Order-Machine: An Open Source

Synthesizable High-Performance RISC-V Processor

Presenter(s): Jerry Zhao, Abe Gonzalez, Ben Korpan

Start: HH\_MM\_SS

16:00 (4:00 pm)

Length: 0:30:0

End: HH\_MM\_SS

16:30 (4:30 pm)

id: 14810 conf: 12

Presenter email:

misc.

Edit

Review

Sound check

Title:  FireSim: Open-Source Easy-to-use FPGA-Accelerated

Cycle-Exact Hardware Simulation in the Cloud

Presenter(s): David Biancolin, Alon Amid

Start: HH\_MM\_SS

16:30 (4:30 pm)

Length: 0:30:0

End: HH\_MM\_SS

17:00 (5:00 pm)

id: 14811 conf: 13

Presenter email:

misc.

Edit

Review

Sound check

Title:  How I started learning FPGA: My journey writing a

GameBoy in Verilog

Presenter(s): Wenting Zhang

Start: HH\_MM\_SS

17:30 (5:30 pm)

Length: 0:30:0

End: HH\_MM\_SS

18:00 (6:00 pm)

id: 14812 conf: 14

Presenter email:

misc.

Edit

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Sound check

Unscheduled

Start: \_\_\_\_\_

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Added to Veyepar

End: \_\_\_\_\_

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Sound check

Title:  Emulation of vintage integrated circuits through die analysis and reverse-EDA

Presenter(s): Cole Johnson

Start: HH\_MM\_SS

18:00 (6:00 pm)

Length: 0:30:0

End: HH\_MM\_SS

18:30 (6:30 pm)

id: 14813 conf: 15

Presenter email:

misc.

Edit  Review

Sound check

Title:  Mingle, drinks

Presenter(s):

Start: HH\_MM\_SS

18:20 (6:20 pm)

Length: 0:30:0

End: HH\_MM\_SS

18:50 (6:50 pm)

id: 14814 conf: 16

Released:  yes  no.

Presenter email:

Presenter initials for intent to release: \_\_\_\_\_

misc.

Edit  Review

NOT Released

Sound check

Unscheduled

Start: \_\_\_\_\_

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Added to Veyepar

End: \_\_\_\_\_

Released:  yes  no.

Presenter initials for intent to release: \_\_\_\_\_

Edit  Review

Equipment problems

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revolution May 05 Sun  
Revolution Hall  
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Kit No:	Director:	Cam Op:	Audio op:	File count: _____
<b>Pre-Day check:</b> <input type="checkbox"/> Replace Batteries in mics Sound levels: <input type="checkbox"/> L-PA <input type="checkbox"/> R-cam mic <input type="checkbox"/> System date and time.			<b>Post production:</b> Edit by: Review by:	<input type="checkbox"/> Files synked <input type="checkbox"/> Set Scanned <input type="checkbox"/> Edit - set done <input type="checkbox"/> Review - done

Sound check Title:  BaseJump STL: a Standard Template Library for Hardware Design

Presenter(s): Daniel Petrisko

Start: HH\_MM\_SS

09:30 (9:30 am)

Length: 0:30:0

End: HH\_MM\_SS

10:00 (10:00 am)  
id: 14815 conf: 17

Presenter email:

misc.

Edit  Review

Sound check Title:  OSVVM, VHDL's #1 FPGA Verification Library  
Presenter(s): Jim Lewis

Start: HH\_MM\_SS

10:00 (10:00 am)

Length: 0:30:0

End: HH\_MM\_SS

10:30 (10:30 am)  
id: 14816 conf: 18

Presenter email:

misc.

Edit  Review

Sound check Title:  Verifying Open-Source Silicon with SystemVerilog:  
Getting in on the Ground Floor

Presenter(s): Matthew Ballance

Start: HH\_MM\_SS

10:30 (10:30 am)

Length: 0:30:0

End: HH\_MM\_SS

11:00 (11:00 am)  
id: 14817 conf: 19

Presenter email:

misc.

Edit  Review

Sound check

Start: \_\_\_\_\_

End: \_\_\_\_\_

Unscheduled

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Edit  Review

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Sound check

Title:  RISC-V in Debian  
Presenter(s): Vagrant Cascadian

Start: HH\_MM\_SS

11:20 (11:20 am)

Length: 0:20:0

End: HH\_MM\_SS

11:40 (11:40 am)  
id: 14818 conf: 20

Presenter email:

misc.

Edit  Review

Sound check

Title:  Lightning Talks  
Presenter(s):

Start: HH\_MM\_SS

11:40 (11:40 am)

Length: 0:30:0

End: HH\_MM\_SS

12:10 (12:10 pm)  
id: 14819 conf: 21

Released:  yes  no.

Presenter email:

Presenter initials for intent to release: \_\_\_\_\_

misc.

Edit  Review

Sound check

Title:  Hermes-Lite: Amateur Radio SDR  
Presenter(s): Steve Haynal

Start: HH\_MM\_SS

11:49 (11:49 am)

Length: 00:12:00

End: HH\_MM\_SS

12:01 (12:01 pm)  
id: 14826 conf:

Presenter email:

SDR misc.

Edit  Review

Sound check

Title:  Tim has too many projects - LatchUp Edition  
Presenter(s): Tim 'mithro' Ansell

Start: HH\_MM\_SS

12:01 (12:01 pm)

Length: 00:28:00

End: HH\_MM\_SS

12:29 (12:29 pm)  
id: 14827 conf:

Presenter email:

fpga SymbiFlow misc.

Edit  Review

Sound check

Lighting Talks  
Keynote

Start: \_\_\_\_\_

End: \_\_\_\_\_

misc.

Added to Veyepar

Edit  Review

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**revolution**    **May 05 Sun**  
**Revolution Hall**  
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Sound check

Title:  FuseSoC - Cores have never been so much fun  
Presenter(s): Olof Kindgren

Start: HH\_MM\_SS  
13:20 (1:20 pm)

Length: 0:30:0

End: HH\_MM\_SS

13:50 (1:50 pm)  
id: 14820 conf: 22

Presenter email:

misc.

Edit     Review

Sound check

Title:  DUH: document and tools for HW design reuse  
Presenter(s): Aliaksei Chapyzenka

Start: HH\_MM\_SS  
13:50 (1:50 pm)

Length: 0:30:0

End: HH\_MM\_SS

14:20 (2:20 pm)  
id: 14821 conf: 23

Presenter email:

misc.

Edit     Review

Sound check

Title:  Nyuzi: An Open Source GPGPU Processor  
Presenter(s): Jeff Bush

Start: HH\_MM\_SS  
14:20 (2:20 pm)

Length: 0:30:0

End: HH\_MM\_SS

14:50 (2:50 pm)  
id: 14822 conf: 24

Presenter email:

misc.

Edit     Review

Sound check

Title:  OpenRAM: An Open Source Memory Compiler  
Presenter(s): Matthew Guthaus

Start: HH\_MM\_SS  
15:10 (3:10 pm)

Length: 0:30:0

End: HH\_MM\_SS

15:40 (3:40 pm)  
id: 14823 conf: 25

Presenter email:

misc.

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Sound check

**Unscheduled**

Start: \_\_\_\_\_

Lighting Talks  
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End: \_\_\_\_\_

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Sound check

Title:  SYZYGY: An Open Standard For Semiconductor Evaluation

Presenter(s): Tom McLeod

Start: HH\_MM\_SS

15:40 (3:40 pm)

Length: 0:30:0

End: HH\_MM\_SS

16:10 (4:10 pm)  
id: 14824 conf: 26

Presenter email:

misc.

Edit  Review

Sound check

Title:  Open-Source FPGA tools, how and why?  
Presenter(s): Piotr Esden-Tempski

Start: HH\_MM\_SS

16:00 (4:00 pm)

Length: 0:20:0

End: HH\_MM\_SS

16:20 (4:20 pm)  
id: 14825 conf: 27

Presenter email:

misc.

Edit  Review

Sound check

Unscheduled

Start: \_\_\_\_\_

Lighting Talks

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misc.

Added to Veyepar

End: \_\_\_\_\_

Edit  Review