

# Fossi Foundation: Latch-Up 2023

## Recording Sheet

☐ Equipment problems

☐ Notes on back

henley1010

Apr 01 Sat

Henley Hall room 1010

set page 1 of 7

Kit No:	Director:	Cam Op:	Audio op:	File count: _____
<b>Pre-Day check:</b> <input type="checkbox"/> Replace Batteries in mics Sound levels: <input type="checkbox"/> L-PA <input type="checkbox"/> R-cam mic <input type="checkbox"/> System date and time.		<b>Post production:</b> <input type="checkbox"/> Files synked Edit by: <input type="checkbox"/> Set Scanned Review by: <input type="checkbox"/> Edit - set done <input type="checkbox"/> Review - done		

☐ Sound check

Title: ☐ A UCSC Professor's path through Open-Source

Hardware

Presenter(s): Jose Renau

Start: HH\_MM\_SS

08:30 (8:30 am)

Length: 00:20:00

End: HH\_MM\_SS

08:50 (8:50 am)

id: 15229 conf: 8

Presenter email:

☐ ☐ ☐ ☐ misc.

☐ Edit

☐ Review

☐ Sound check

Title: ☐ So You Want to be an Open Sourcerer?

Presenter(s): Dustin Richmond

Start: HH\_MM\_SS

08:55 (8:55 am)

Length: 00:20:00

End: HH\_MM\_SS

09:15 (9:15 am)

id: 15230 conf: 9

Presenter email:

☐ ☐ ☐ ☐ misc.

☐ Edit

☐ Review

☐ Sound check

Title: ☐ If You Build It, Who Will Come?

Presenter(s): Scott Beamer

Start: HH\_MM\_SS

08:55 (8:55 am)

Length: 00:20:00

End: HH\_MM\_SS

09:15 (9:15 am)

id: 15232 conf: 40

Presenter email:

☐ None ☐ ☐ ☐ ☐ misc.

☐ Edit

☐ Review

☐ Sound check

Start: \_\_\_\_\_

Unscheduled

Lighting Talks

Keynote

☐ ☐ ☐ ☐ misc.

☐ Added to Veyepar

End: \_\_\_\_\_

☐ Edit

☐ Review

Latch-Up 2023

set page 1 of 7

# Fossi Foundation: Latch-Up 2023

## Recording Sheet

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henley1010

Apr 01 Sat

Henley Hall room 1010

set page 2 of 7

☐ Sound check

Title: ☐ Yet Another UCSC Professor's path through  
Open-Source Hardware

Presenter(s): Matt Guthaus

Start:

09:20 (9:20 am)

Length: 00:20:00

End:

09:40 (9:40 am)

id: 15233 conf: 41

Presenter email:

☐ ☐ ☐ ☐ misc.

☐ Edit

☐ Review

☐ Sound check

Title: ☐ Tapeout-in-a-Semester: The Organization of Berkeley's  
Tapeout Course

Presenter(s): Jerry Zhao

Start:

09:45 (9:45 am)

Length: 00:20:00

End:

10:05 (10:05 am)

id: 15234 conf: 42

Presenter email:

☐ ☐ ☐ ☐ misc.

☐ Edit

☐ Review

☐ Sound check

Title: ☐ Fearless Hardware Design  
CPU

Presenter(s): Rachit Nigam

Start:

10:10 (10:10 am)

Length: 00:20:00

End:

10:30 (10:30 am)

id: 15254 conf: 29

Presenter email:

☐ ☐ ☐ ☐ misc.

☐ Edit

☐ Review

☐ Sound check

Title: ☐ Minimax - a Compressed-First, Microcoded RISC-V  
CPU

Presenter(s): Graeme Smecher

Start:

11:00 (11:00 am)

Length: 00:20:00

End:

11:20 (11:20 am)

id: 15235 conf: 10

Presenter email:

☐ ☐ ☐ ☐ misc.

☐ Edit

☐ Review

☐ Sound check

Start:

Lighting Talks

Keynote

☐ ☐ ☐ ☐ misc.

☐ Added to Veyepar

End:

☐ Edit

☐ Review

Latch-Up 2023

set page 2 of 7

# Fossi Foundation: Latch-Up 2023

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Apr 01 Sat

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set page 3 of 7

☐ Sound check

Title: ☐ Open Source Brain-Inspired Neuromorphic Software and Hardware

Presenter(s): Jason Eshraghian

Start: HH\_MM\_SS

11:30 (11:30 am)

Length: 00:20:00

End: HH\_MM\_SS

11:50 (11:50 am)

id: 15236 conf: 11

Presenter email:

☐ None ☐ misc.

☐ Edit

☐ Review

☐ Sound check

Title: ☐ Gdsfactory, an Open Source python flow for circuit design, verification and validation

Presenter(s): Joaquin Matres

Start: HH\_MM\_SS

12:57 (12:57 pm)

Length: 00:3:00

End: HH\_MM\_SS

13:00 (1:00 pm)

id: 15255 conf: -1

Presenter email:

☐ [ ] [ ] [ ] [ ] misc.

☐ Edit

☐ Review

☐ Sound check

Title: ☐ RISu064 - An in-order non-blocking dual-issue RISC-V 64 processor

Presenter(s): Wenting Zhang

Start: HH\_MM\_SS

13:00 (1:00 pm)

Length: 00:3:00

End: HH\_MM\_SS

13:03 (1:03 pm)

id: 15237 conf: 12

Presenter email:

☐ None ☐ misc.

☐ Edit

☐ Review

☐ Sound check

Title: ☐ SimCommand: A High-Performance RTL Testbench API with Fork/Join Support

Presenter(s): Vighnesh Iyer

Start: HH\_MM\_SS

13:03 (1:03 pm)

Length: 00:3:00

End: HH\_MM\_SS

13:06 (1:06 pm)

id: 15238 conf: 13

Presenter email:

☐ [ ] [ ] [ ] [ ] misc.

☐ Edit

☐ Review

☐ Sound check

Start: \_\_\_\_\_

Lighting Talks

Keynote

☐ [ ] [ ] [ ] [ ] misc.

☐ Added to Veyepar

End: \_\_\_\_\_

☐ Edit

☐ Review

Latch-Up 2023

set page 3 of 7

☐ Equipment problems☐ Notes on back

henley1010

Apr 01 Sat

Henley Hall room 1010

set page 4 of 7

☐ Sound checkTitle: ☐ A radically simple 1-bit single-die "supercomputer"

Presenter(s): James Bowman

Start: HH\_MM\_SS

13:06 (1:06 pm)

Length: 00:3:00

End: HH\_MM\_SS

13:09 (1:09 pm)

id:15239 conf: 14

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ All Your OSS Codebase Are Belong To Us

Presenter(s): Oron Port

Start: HH\_MM\_SS

13:09 (1:09 pm)

Length: 00:3:00

End: HH\_MM\_SS

13:12 (1:12 pm)

id:15240 conf: 15

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ A Modular Approach to Variable Pipeline Depth Designs

Presenter(s): Peter Herrmann

Start: HH\_MM\_SS

13:12 (1:12 pm)

Length: 00:3:00

End: HH\_MM\_SS

13:15 (1:15 pm)

id:15241 conf: 16

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Open-source Hardware for Real-Time Applications

Presenter(s): Francisco Wilken

Start: HH\_MM\_SS

13:15 (1:15 pm)

Length: 00:3:00

End: HH\_MM\_SS

13:18 (1:18 pm)

id:15242 conf: 17

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound check

Start: \_\_\_\_\_

Lighting Talks

Keynote

☐ ☐ ☐ ☐ misc.☐ Added to Veyepar

End: \_\_\_\_\_

☐ Edit☐ Review

☐ Equipment problems☐ Notes on back

henley1010

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set page 5 of 7

☐ Sound checkTitle: ☐ An open-source robotics applications interface

Presenter(s): Nikita Klimov

Start: HH\_MM\_SS

13:18 (1:18 pm)

Length: 00:3:00

End: HH\_MM\_SS

13:21 (1:21 pm)

id: 15243 conf: 18

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ DSAGEN: Democratizing Domain-Specific Accelerator

Generation for FPGA Overlays

Presenter(s): Dylan Kupsh

Start: HH\_MM\_SS

13:21 (1:21 pm)

Length: 00:3:00

End: HH\_MM\_SS

13:24 (1:24 pm)

id: 15256 conf: 50

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Educational Framework for Functional Verification

Presenter(s): Matthew Michilot

Start: HH\_MM\_SS

13:24 (1:24 pm)

Length: 00:3:00

End: HH\_MM\_SS

13:27 (1:27 pm)

id: 15257 conf: 51

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Using CVA6 in Architecture Education

Presenter(s): Ethan Sifferman

Start: HH\_MM\_SS

13:27 (1:27 pm)

Length: 00:3:00

End: HH\_MM\_SS

13:30 (1:30 pm)

id: 15258 conf: 52

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound check

Unscheduled

Start: \_\_\_\_\_

Lighting Talks

Keynote

☐ ☐ ☐ ☐ misc.☐ Added to Veyepar

End: \_\_\_\_\_

Released: ☐ yes ☐ no.

Presenter initials for intent to release: \_\_\_\_\_

☐ Edit☐ Review

**Fossi Foundation: Latch-Up 2023****Recording Sheet**☐ Equipment problems☐ Notes on back

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Apr 01 Sat

↳ Henley Hall room 1010

set page 6 of 7

☐ Sound checkTitle: ☐ Open tools for an open standard: Control/status register automation using SystemRDL

Presenter(s): Alex Mykyta

Start: 

13:30 (1:30 pm)

Length: 00:20:00

End: Released: ☐ yes ☐ no.  

13:50 (1:50 pm)

id: 15244 conf: 19

Presenter email:

Presenter initials for intent to release: ☐ Sound checkTitle: ☐ Goodbye Make, Hello SiliconCompiler!

Presenter(s): Andreas Olofsson

Start: 

14:00 (2:00 pm)

Length: 00:20:00

End: 

Presenter email:

  

14:20 (2:20 pm)

id: 15245 conf: 20

☐ Sound checkTitle: ☐ Chipyard: An Open-Source RISC-V SoC Design Framework

Presenter(s): Abraham Gonzalez

Start: 

15:00 (3:00 pm)

Length: 00:20:00

End: 

Presenter email:

  

15:20 (3:20 pm)

id: 15246 conf: 21

☐ Sound checkTitle: ☐ SVA based Checker IP in Verilator

Presenter(s): Balram, Srinivasan Venkataramanan

Start: 

15:30 (3:30 pm)

Length: 00:20:00

End: 

Presenter email:

  

15:50 (3:50 pm)

id: 15247 conf: 22

☐ Sound check

Lighting Talks

Keynote

Start:   End:

# Fossi Foundation: Latch-Up 2023

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set page 7 of 7

☐ Sound check

Title: ☐ Quality of Life with Virtual Prototypes - Open Source

Tools in the Ecosystem of the RISC-V VP

Presenter(s): Pascal Pieper

Start:

HH\_MM\_SS

16:00 (4:00 pm)

Length: 00:20:00

End:

HH\_MM\_SS

16:20 (4:20 pm)

id: 15248 conf: 23

Presenter email:

☐ ☐ ☐ ☐ misc.

☐ Edit

☐ Review

☐ Sound check

Start:

\_\_\_\_\_

# Unscheduled

Lighting Talks

Keynote

End:

\_\_\_\_\_

☐ ☐ ☐ ☐ misc.

☐ Added to Veyepar

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☐ Review