Fossi Foundation	: Latch_2024		Recording Sheet		
[] Equipment probler	ns [] Notes on	back	ven	ncaf Apr 19 Fri VenCaf set page 1 of 11	
Kit No: Direct	or: Cam Op:	Audio op:		File count:	
Pre-Day check: [] Replace Batterie: Sound levels: [] L- [] System date and	PA [] R-cam mic	Post production: Edit by: Review by:]]]] Files synked] Set Scanned] Edit - set done] Review - done	
	Title: [_] Application of Al-co		oftwa	are	
Start: HH MM SS 19:00 (7:00 pm) Length: 00:3:00	riesenter(s). Wax i aramaiz	•			
End:	Presenter email:		[]	[] [] misc.] Edit [] Review	
	Title: [_] Attempts to design	hardware using dafny			
Start: HH_MM_SS 19:03 (7:03 pm)	Presenter(s): Ben Reynwar				
Length: 00:3:00					
End:	Presenter email:		[]	[] [] misc. Edit [] Review	
	Title: [_] Accelerating Hardw		m Gl	PTs	
Start: HH_MM_SS 19:06 (7:06 pm)	Presenter(s): Prakash Shvet	alik			
Length: 00:3:00					
End:	Presenter email:		[]	[] [] misc. Edit [] Review	
[] Sound check Start:	Unsched Lighting Talks	duled		[][][] misc.	
End.	Keynote			Added to Veyepar	
End: Latch_2024				Edit [] Review set page 1 of 11	

Fossi Foundation	n: Latch_20)24	Reco	rding Sheet
[] Equipment proble	ems	[] Notes on back	vencaf	Apr 19 Fri VenCaf set page 2 of 11
[] Sound check		IHISS Open Source for Hybrid I	Hetrogenous	
Start:	Presenter(s):	John Goodenough		
Length: 00:3:00				
End: HH_MM_S				[][] [] misc.
19:12 (7:12 pm) id:15390 conf: 35	Presenter email	:	[] Edit	[] Review
[] Sound check		n up your EDA flows with tclint Noah Moroze		
Start: HH_MM_S	<u>S</u>			
19:12 (7:12 pm)				
Length: 00:3:00	S			
End:	Presenter email	:	[···	[][] misc.
id: 15391 conf: 36			[] Edit	[] Review
[] Sound check		ling an Ecosystem for Hardward Rachit Nigam	e Generators	
Start: HH_MM_S	S	Tido.iii Tilgaiii		
19:15 (7:15 pm)	_			
Length: 00:3:00				
End: HH_MM_S				[][][] misc.
19:18 (7:18 pm) id:15392 conf: 37	Presenter email	:	[] Edit	[] Review
[] Sound check	Title: [_] Citize furnace for Si	enSilicon: towards an open-sou growth	urce Czochralski	
Start: HH_MM_S	Presenter(s):			
19:18 (7:18 pm)	_			
Length: 00:3:00				
End: HH_MM_S				[][] [] misc.
19:21 (7:21 pm) id:15393 conf: 38	Presenter email	:	[] Edit	[] Review
[] Sound check		scheduled		
Start:	Lighting 7	lalks		[][] [] misc.
	Keynote		[] Add	ed to Veyepar
End:	_		[] Edit	[] Review
Latch_2024				set page 2 of 11

Fossi Foundat	ion: Latch_20	024		F	Recordir	ng Sheet
[] Equipment pro	oblems [] Notes on back			b45r		Apr 19 Fri b45r230 page 3 of 11
Kit No: Di	rector:	Cam Op:	Audio op:		File cou	nt:
Pre-Day check: [] Replace Batteries in mics Sound levels: [] L-PA [] R-cam mic [] System date and time. Post production Edit by: Review by:]]]] Files sy] Set Sca] Edit - s] Review	anned
[] Sound check Start: HH		ter: An Open-so Wenting Zhang	ource E-Ink Controlle	r		
End: 09:40 (9:40 am) id:15356 conf: 1	Presenter emai	l:		[]] Review
Start: HH	University Co		DA using a Tapeout			
End:	Presenter emai	l:] Review
[] Sound check Start: HH	Presenter(s):	arEDA for open Keno Fischer	source silicon			
End: 10:40 (10:40 am) id:15358 conf: 3	SS Presenter emai	l:		[]		[][] misc.
[] Sound check Start: End:	Lighting Keynote	sche (Talks	duled	[]	Added to	[][] misc. o Veyepar] Review
Latch_2024					<u> </u>	page 3 of 11

Fossi Foundation	n: Latch_20)24	Reco	rding Sheet
[] Equipment proble	ems	[] Notes on back	b45r230	Apr 19 Fri b45r230 set page 4 of 11
[] Sound check		ort: Software-Oriented Accele	ration for You, Me,	
Start:		Nazerke Turtayeva		
Length: 00:20:00				
End: HH_MM_S	Presenter email			[][] misc.
11:00 (11:00 am) id:15359 conf: 4	T 1000mor oman	•	[] Edit	[] Review
[] Sound check		ards xBGAS on CHERI: Exan	nining the Benefits	of a
Start:	-	West Side		
11:00 (11:00 am)				
Length: 00:20:00	e			
End: HH_MM_S	Presenter email	:		[][] misc.
11:20 (11:20 am) id:15360 conf: 5		•	[] Edit	[] Review
[] Sound check	Title: [_] Toward Presenter(s):	ards Cycle-accurate Simulatio Jie Li	on of xBGAS	
Start: HH_MM_S	S			
12:20 pm)				
Length: 00:20:00				
End: HH_MM_S				[][] misc.
12:40 (12:40 pm) id:15361 conf: 6	Presenter email	:	[] Edit	[] Review
[] Sound check	Title: [_] Artifa	act Evaluation for the Field Pr	ogrammable Gate	
Start: HH_MM_S		Miriam Leeser		
12:40 (12:40 pm)	_			
Length: 00:20:00				
End: HH_MM_S	S			[][][] misc.
13:00 (1:00 pm) id:15362 conf: 7	Presenter email	:	[] Edit	
[] Sound check	Uns	schedule		<u>[] : : : : : : : : : : : : : : : : : : </u>
Start:	Lighting			[][][] misc.
	Keynote		[] Add	ed to Veyepar
End:	_		[] Edit	
Latch_2024			[[]-4.0	set page 4 of 11

Fossi Foundation: Latch_2024				ding Sheet
[] Equipment prob	olems	[] Notes on back	b45r230	Apr 19 Fri b45r230 set page 5 of 11
[] Sound check Start: HH		sel 6 and beyond : Jack Koenig		
End: 13:20 (1:20 pm) id:15363 conf: 8	Presenter ema	il:	[] Edit	[] [] misc.
[] Sound check Start: 13:20 (1:20 pm) Length: 00:20:00	Verification T Presenter(s) —	PHS: Enabling Transaction-lev Through PDVL : Tobias Strauch	el Deductive Form	al
End: 13:40 (1:40 pm) id:15364 conf: 9	Presenter ema	il:	[] Edit	[][] misc. [] Review
[] Sound check Start:	Models and	nitecture 2.0: Toward Open So Datasets for Hardware Design : Vijay Janapa Reddi, Shvetan		
End: 14:40 (2:40 pm) id:15365 conf: 10	Presenter ema	il:	[] Edit	[] Review
[] Sound check Start:	Presenter(s) 	ng The Wave: Building Wave F : Rice Shelley	Pipelines in FPGAs	3
End:	Presenter ema	il:	[] Edit	[][] misc. [] Review
[] Sound check Start:	Lighting Keynote	scheduled	l 1 Adde	[][][] misc.
End:			[] Edit	[] Review
Latch_2024			<u>, = =</u>	set page 5 of 11

Fossi Foundati	ion: Latch_2024	Recording Sheet
[] Equipment pro	blems [] Notes on back	b45r230 Apr 19 Fri b45r230 set page 6 of 11
[] Sound check Start: HH MM 15:00 (3:00 pm) Length: 00:20:00	Title: [_] Giving Students A Byte of Open- Hardware Education Presenter(s): Ethan Sifferman	Source: Advancing
End: 15:20 (3:20 pm) id:15367 conf: 12	Presenter email:	[] Edit [] Review
[] Sound check Start: HH_MM 15:40 (3:40 pm) Length: 00:20:00	Title: [_] Open-source resources for learn HL-HDLs Presenter(s): Rishiyur Nikhil	ing the Bluespec
	Presenter email:	[] Edit [] Review
[] Sound check Start: HH 16:00 (4:00 pm) Length: 00:20:00	Title: [_] PyHDL-IF: An Easy-to-Use Pytho Interface SPresenter(s): Matt Ballance —	on/HDL Cross-Calling
End: 16:20 (4:20 pm) id:15369 conf: 14	Presenter email:	[] Edit [] Review
[] Sound check Start:	Unschedule Lighting Talks Keynote	[] Added to Veyepar
End:		[] Edit [] Review

set page 6 of 11

Latch_2024

Fossi Founda	tion: Latch_2	024		F	Recordi	ing Sheet
[] Equipment pro	oblems	[] Notes on	back	b45r230 Apr 20 b45r set page 7 c		
Kit No: D	irector:	Cam Op:	Audio op:		File cou	unt:
Pre-Day check: [] Replace Bat Sound levels: [[] System date	Post production: Edit by: Review by:]]]] Edit -	synked canned set done w - done		
[] Sound check		en source RTL v	erification with Verila	itor		
Start:	SS	. Naioi Gugaia				
End: 09:20 (9:20 am) id:15370 conf: 15	Presenter ema	ail:][][]misc.
[] Sound check			nent platform to enab	ole exp	loring the	9
Start: 09:20 (9:20 am)	use of CHEI Presenter(s)	RI for embedded : Hugo McNally	applications			
Length: 00:20:00						
End:	Presenter ema	ail:			[]] Edit	[] Review
[] Sound check	Title: [_] Tra RISC-V	nsparent Check	pointing for Fault Tol	erance	e in	
Start:	Presenter(s)	: Aayushi Gauta	m			
Length: 00:20:00						
End:	Presenter ema	ail:				[] Review
[] Sound check Start:	Lighting	Talks	duled][][] misc.
Fad	Keynote				•	to Veyepar
End: Latch_2024				[]] Edit	[] Review
Lattin_2024					se	t page 7 of 11

Fossi Foundation	n: Latch_20	024	Reco	Recording Sheet		
[] Equipment problems [] Notes on back			b45r230	Apr 20 Sat b45r230 set page 8 of 11		
[] Sound check	Title: [_] HDL LLMs	Agent, Enhancing Hardware L	anguage in the ag	je of		
Start: HH MM S 10:20 (10:20 am) Length: 00:20:00	Presenter(s):	Jose Renau				
End: HH_MM_S	S					
10:40 (10:40 am)	Presenter email	l:	[] Edit	[][] misc.		
id: 15373 conf: 18				[] Veniew		
[] Sound check	Languages	de: An HDL Inspired By Mode	rn Software			
Start: HH_MM_S	Presenter(s):	Frans Skarman				
10:40 (10:40 am)	_					
Length: 00:20:00						
End: HH_MM_S				[][] [] misc.		
11:00 (11:00 am) id:15374 conf: 19	Presenter email	l:	[] Edit	[] Review		
[] Sound check		chboard: Calling All Hardware Steven Herbst	Models			
Start: HH_MM_S	S					
11:00 (11:00 am)	_					
Length: 00:20:00						
End: HH_MM_S	S			[][][] misc.		
11:20 (11:20 am) id:15375 conf: 20	Presenter email	l :	[] Edit			
[] Sound check	Title: [] Fron	n an Open-Source ISA to Ope				
IIII MM C	Open-Source	Silicon				
Start: HH_WW_S	- -	Luca Bertaccini				
12:20 (12:20 pm)						
Length: 00:20:00	C					
End: HH_MM_S	Presenter email	l:		[][] misc.		
12:40 (12:40 pm) id:15376 conf: 21			[] Edit	[] Review		
[] Sound check	Uns	schedule				
Start:	Lighting	Talks		[][][] misc.		
	Keynote		[] Add	ed to Veyepar		
End:			[] Edit	[] Review		
Latch_2024				set page 8 of 11		

Fossi Foundation	n: Latch_20)24	Reco	Recording Sheet		
[] Equipment proble	ems	[] Notes on back	b45r230	Apr 20 Sat b45r230 set page 9 of 11		
[] Sound check	Title: [_] Oper (instead of pr	n Source Hardware: Hacking S	ilicon for Fun			
Start:		Troy Benjegerdes				
Length: 00:20:00						
End: HH_MM_S	Presenter email			[][][] misc.		
13:00 (1:00 pm) id:15377 conf: 22	T resenter email		[] Edit	[] Review		
[] Sound check	Projects under	story of TL-Verilog Google Sumer FOSSi Foundation Steve Hoover	nmer of Code			
Start:	<u>-</u>	Steve Hoovel				
13:00 (1:00 pm) Length: 00:20:00						
	S					
13:20 (1:20 pm)	Presenter email	:	[] Edi4	[][] misc.		
id: 15378 conf: 23			[] Edit	[] Review		
[] Sound check		Universal Memory Interface Andreas Olofsson				
Start: HH_MM_S	S					
13:20 (1:20 pm)	_					
Length: 00:20:00						
End: HH_MM_S		L		[][] [] misc.		
13:40 (1:40 pm) id:15379 conf: 24	Presenter email		[] Edit	[] Review		
[] Sound check		: The Way It Should Have Beer Alan Mishchenko	n Designed			
Start: HH_MM_S	<u>S</u>					
14:20 (2:20 pm)						
Length: 00:20:00						
End: HH_MM_S	Presenter email	•		[][] misc.		
14:40 (2:40 pm) id:15380 conf: 25	Troomer email		[] Edit	[] Review		
[] Sound check		scheduled				
Start:	Lighting ⁻	Talks		[][] misc.		
	Keynote		[] Add	ed to Veyepar		
End:			[] Edit	[] Review		
Latch_2024				set page 9 of 11		

Fossi Foundation: Latch_2024				Recording Sheet		
[] Equipment probl	lems	[] Notes on back	b45r230	Apr 20 Sat b45r230 set page 10 of 11		
[] Sound check Start: HH_MM_S 14:40 (2:40 pm)	core code de	oL (Build Your Own Linter) – U velopment Ajeetha Kumari Venkatesan	JVMLint for IEEE-U	VM		
Length: 00:20:00						
End:	Presenter email	:	[] Edit	[][] misc. [] Review		
Start: HH MM S 15:00 (3:00 pm)		ond EDA lies Edalize Olof Kindgren				
Length: 00:20:00						
End:	Presenter email	:	[] Edit	[] [] misc.		
Start: HH_MM_S 15:40 (3:40 pm)	application	Front-end receiver design for 2 Jabeom Koo	2.4GH/5GHz WiFi			
End: 50:20:00 End: 500 (4:00 pm) id:15383 conf: 28	Presenter email	:	[] Edit	[][] misc. [] Review		
Start: HH_MM_S 16:00 (4:00 pm)	design flow	E Study: Open source analog Tim Edwards	g and mixed-signal			
End:S 16:20 (4:20 pm) id:15384 conf: 29	S Presenter email	:	[] Edit	[][] misc.		
[] Sound check	Uns	scheduled	d			
Start:	_ Lighting ⁻ Keynote	I ains	r 1 A al al a	[][][]misc.		
End:	Reynote			ed to Veyepar		
Latch_2024			[] Edit	[] Review set page 10 of 11		

Fossi Foundat	Fossi Foundation: Latch_2024			
[] Equipment pro	oblems	[] Notes on back	b45r230	Apr 20 Sat b45r230 set page 11 of 11
[] Sound check Start: HH 16:20 (4:20 pm) Length: 00:20:00	State an	IHP Open Source PDK: Announcem d Experiences, and look ahead er(s): Frank Vater	ent, Setup, Cur	rent
End:	Presenter	email:	[] Edit	[][][]misc.
[] Sound check Start: HH MM 16:40 (4:40 pm)	Title: [_] Presente	Tiny Tapeout: custom silicon open to er(s): Pat Deegan) all	
End:	Presenter	email:	[] Edit	[][][]misc.
[] Sound check Start: End:	1.2 - 1.4	nscheduled ing Talks iote	[] Add	[][][] misc. ed to Veyepar

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Latch_2024