

Fossi Foundation: Latch_2024

Recording Sheet

[] Equipment problems

[] Notes on back

vencaf

Apr 19 Fri

VenCaf

set page 1 of 13

Kit No:	Director:	Cam Op:	Audio op:	File count: _____
Pre-Day check: [] Replace Batteries in mics Sound levels: [] L-PA [] R-cam mic [] System date and time.		Post production: [] Files synked Edit by: [] Set Scanned Review by: [] Edit - set done [] Review - done		

[] Sound check

Title: [] CitizenSilicon: towards an open-source Czochralski furnace for Si growth

Presenter(s): Max Aalto

Start: HH_MM_SS

18:00 (6:00 pm)

canceled

Length: 00:3:00

End: HH_MM_SS

18:03 (6:03 pm)

id: 15393 conf: 38

Presenter email:

[] [] [] [] misc.

[] Edit

[] Review

[] Sound check

Title: [] Building an Ecosystem for Hardware Generators

Presenter(s): Rachit Nigam

Start: HH_MM_SS

18:00 (6:00 pm)

canceled

Length: 00:3:00

End: HH_MM_SS

18:03 (6:03 pm)

id: 15392 conf: 37

Presenter email:

[] [] [] [] misc.

[] Edit

[] Review

[] Sound check

Title: [] Application of AI-computer vision based software

Presenter(s): Max Faramarzi

Start: HH_MM_SS

18:00 (6:00 pm)

canceled

Length: 00:3:00

End: HH_MM_SS

18:03 (6:03 pm)

id: 15387 conf: 32

Presenter email:

[] [] [] [] misc.

[] Edit

[] Review

[] Sound check

Start: _____

Unscheduled

Lighting Talks

Keynote

[] [] [] [] misc.

[] Added to Veyepar

End: _____

[] Edit

[] Review

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☐ Sound checkTitle: ☐ Tim's Silicon Presentations

Presenter(s): Tim Ansell

Start: HH_MM_SS

18:14 (6:14 pm)

Length: 00:3:00

End: HH_MM_SS

18:17 (6:17 pm)

id: 15394 conf: 100

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Surfer - An Extensible and Snappy Waveform Viewer

Presenter(s): Frans Skarman

Start: HH_MM_SS

18:18 (6:18 pm)

Length: 00:3:00

End: HH_MM_SS

18:21 (6:21 pm)

id: 15395 conf: 101

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Clean up your EDA flows with tclint

Presenter(s): Noah Moroze

Start: HH_MM_SS

18:21 (6:21 pm)

Length: 00:3:00

End: HH_MM_SS

18:24 (6:24 pm)

id: 15391 conf: 36

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Attempts to design hardware using dafny

Presenter(s): Ben Reynwar

Start: HH_MM_SS

18:24 (6:24 pm)

Length: 00:3:00

End: HH_MM_SS

18:27 (6:27 pm)

id: 15388 conf: 33

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound check

Start: _____

Lighting Talks

Keynote

☐ ☐ ☐ ☐ misc.☐ Added to Veyepar

End: _____

☐ Edit☐ Review

☐ Equipment problems☐ Notes on back

vencaf

Apr 19 Fri

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☐ Sound checkTitle: ☐ Zeno: An Open-Source Scalable Capability-Based
Secure Architecture

Presenter(s): Alan Ehret

Start:

18:27 (6:27 pm)

Length: 00:3:00

End:

18:30 (6:30 pm)

id: 15396 conf: 102

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Accelerating Hardware Design with Custom GPTs
Presenter(s): Prakash ShvetankStart:

18:29 (6:29 pm)

Length: 00:3:00

End:

18:32 (6:32 pm)

id: 15389 conf: 34

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ OSHHISS Open Source for Hybrid Hetrogenous
Integrated Semiconductor Systems

Presenter(s): John Goodenough

Start:

18:32 (6:32 pm)

Length: 00:3:00

End:

18:35 (6:35 pm)

id: 15390 conf: 35

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Where Community Powers Innovation
Presenter(s): Mohamed KassemStart:

18:35 (6:35 pm)

Length: 00:3:00

End:

18:38 (6:38 pm)

id: 15397 conf: 103

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkStart:

Lighting Talks

Keynote

☐ ☐ ☐ ☐ misc.☐ Added to VeyeparEnd: ☐ Edit☐ Review

Fossi Foundation: Latch_2024**Recording Sheet**☐ Equipment problems☐ Notes on back

vencaf

Apr 19 Fri

VenCaf

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☐ Sound checkTitle: ☐ CoreScore like never before
Presenter(s): Olof KindgrenStart: HH_MM_SS
18:40 (6:40 pm)

Length: 00:3:00

End: HH_MM_SS18:43 (6:43 pm)
id: 15398 conf: 104

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit ☐ Review☐ Sound checkTitle: ☐ ChipWhisperer: Past & Future of a FPGA-based
Research Tool

Presenter(s): Jean-Pierre Thibault

Start: HH_MM_SS
18:43 (6:43 pm)

Length: 00:3:00

End: HH_MM_SS18:46 (6:46 pm)
id: 15399 conf: 105

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit ☐ Review☐ Sound checkTitle: ☐ clock
Presenter(s): ckStart: HH_MM_SS
19:07 (7:07 pm)

Length: 00:00:20

End: HH_MM_SS19:08 (7:08 pm)
id: 15400 conf:Released: ☐ yes ☐ no.

Presenter email:

Presenter initials for intent to release: _____

☐ None ☐ ☐ ☐ ☐ misc.☐ Edit ☐ Review☐ Sound check

Start: _____

Lighting Talks

Keynote

☐ ☐ ☐ ☐ misc.☐ Added to Veyepar

End: _____

Released: ☐ yes ☐ no.

Presenter initials for intent to release: _____

☐ Edit ☐ Review

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b45r230

Apr 19 Fri

b45r230

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Kit No:	Director:	Cam Op:	Audio op:	File count: _____
Pre-Day check: <input type="checkbox"/> Replace Batteries in mics Sound levels: <input type="checkbox"/> L-PA <input type="checkbox"/> R-cam mic <input type="checkbox"/> System date and time.		Post production: <input type="checkbox"/> Files synked Edit by: <input type="checkbox"/> Set Scanned Review by: <input type="checkbox"/> Edit - set done <input type="checkbox"/> Review - done		

☐ Sound check

Title: ☐ Caster: An Open-source E-Ink Controller
 Presenter(s): Wenting Zhang

Start: HH_MM_SS

09:20 (9:20 am)

Length: 00:20:00

End: HH_MM_SS

09:40 (9:40 am)

id: 15356 conf: 1

Presenter email:

☐ ☐ ☐ ☐ misc.

☐ Edit ☐ Review

☐ Sound check

Title: ☐ Teaching Modern EDA using a Tapeout-Centric
 University Course
 Presenter(s): Anish Singhani

Start: HH_MM_SS

09:40 (9:40 am)

Length: 00:20:00

End: HH_MM_SS

10:00 (10:00 am)

id: 15357 conf: 2

Presenter email:

☐ ☐ ☐ ☐ None misc.

☐ Edit ☐ Review

☐ Sound check

Title: ☐ CedarEDA for open source silicon
 Presenter(s): Keno Fischer

Start: HH_MM_SS

10:20 (10:20 am)

Length: 00:20:00

End: HH_MM_SS

10:40 (10:40 am)

id: 15358 conf: 3

Presenter email:

☐ ☐ ☐ ☐ misc.

☐ Edit ☐ Review

☐ Sound check

Start: _____

End: _____

Unscheduled

Lighting Talks

Keynote

☐ ☐ ☐ ☐ misc.

☐ Added to Veyepar

☐ Edit ☐ Review

Latch_2024

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Apr 19 Fri

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☐ Sound checkTitle: ☐ Cohort: Software-Oriented Acceleration for You, Me,
and Our Heterogeneous SoCs

Presenter(s): Nazerke Turtayeva

Start: HH_MM_SS

10:40 (10:40 am)

Length: 00:20:00

End: HH_MM_SS

11:00 (11:00 am)

id: 15359 conf: 4

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Towards Cycle-accurate Simulation of xBGAS

Presenter(s): Jie Li

Start: HH_MM_SS

10:53 (10:53 am)

Length: 00:20:00

End: HH_MM_SS

11:13 (11:13 am)

id: 15361 conf: 6

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Towards xBGAS on CHERI: Examining the Benefits of a
Secure Distributed Architecture

Presenter(s): Mert Side

Start: HH_MM_SS

11:00 (11:00 am)

Length: 00:20:00

End: HH_MM_SS

11:20 (11:20 am)

id: 15360 conf: 5

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Artifact Evaluation for the Field Programmable Gate
Array Community

Presenter(s): Miriam Leeser

Start: HH_MM_SS

12:40 (12:40 pm)

Length: 00:20:00

End: HH_MM_SS

13:00 (1:00 pm)

id: 15362 conf: 7

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound check

Lighting Talks

Keynote

Start: _____

☐ ☐ ☐ ☐ misc.☐ Added to Veyepar

End: _____

☐ Edit☐ Review

☐ Equipment problems☐ Notes on back

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☐ Sound check Title: ☐ Chisel 6 and beyond
Presenter(s): Jack KoenigStart: HH_MM_SS
13:00 (1:00 pm)

Length: 00:20:00

End: HH_MM_SS13:20 (1:20 pm)
id: 15363 conf: 8

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit ☐ Review☐ Sound check Title: ☐ MRPHS: Enabling Transaction-level Deductive Formal
Verification Through PDVL
Presenter(s): Tobias StrauchStart: HH_MM_SS
13:20 (1:20 pm)

Length: 00:20:00

End: HH_MM_SS13:40 (1:40 pm)
id: 15364 conf: 9

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit ☐ Review☐ Sound check Title: ☐ Architecture 2.0: Toward Open Source Foundation
Models and Datasets for Hardware Design
Presenter(s): Vijay Janapa Reddi, Shvetank PrakashStart: HH_MM_SS
14:20 (2:20 pm)

Length: 00:20:00

End: HH_MM_SS14:40 (2:40 pm)
id: 15365 conf: 10

Presenter email:

☐ None ☐ ☐ ☐ ☐ misc.☐ Edit ☐ Review☐ Sound check Title: ☐ Riding The Wave: Building Wave Pipelines in FPGAs
Presenter(s): Rice ShelleyStart: HH_MM_SS
14:40 (2:40 pm)

Length: 00:20:00

End: HH_MM_SS15:00 (3:00 pm)
id: 15366 conf: 11

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit ☐ Review☐ Sound check

Start: _____

Lighting Talks

Keynote

☐ ☐ ☐ ☐ misc.☐ Added to Veyepar

End: _____

☐ Edit ☐ Review

☐ Equipment problems☐ Notes on back

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☐ Sound checkTitle: ☐ Giving Students A Byte of Open-Source: Advancing
Hardware Education

Presenter(s): Ethan Sifferman

Start: HH_MM_SS

15:00 (3:00 pm)

Length: 00:20:00

End: HH_MM_SS

15:20 (3:20 pm)

id: 15367 conf: 12

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Open-source resources for learning the Bluespec
HL-HDLs

Presenter(s): Rishiyur Nikhil

Start: HH_MM_SS

15:40 (3:40 pm)

Length: 00:20:00

End: HH_MM_SS

16:00 (4:00 pm)

id: 15368 conf: 13

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ PyHDL-IF: An Easy-to-Use Python/HDL Cross-Calling
Interface

Presenter(s): Matt Ballance

Start: HH_MM_SS

16:00 (4:00 pm)

Length: 00:20:00

End: HH_MM_SS

16:20 (4:20 pm)

id: 15369 conf: 14

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound check

Start: _____

Lighting Talks

Keynote

End: _____

☐ ☐ ☐ ☐ misc.☐ Added to Veyepar☐ Edit☐ Review

Unscheduled

☐ Equipment problems☐ Notes on back

b45r230

Apr 20 Sat

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Kit No:	Director:	Cam Op:	Audio op:	File count: _____
Pre-Day check: <input type="checkbox"/> Replace Batteries in mics Sound levels: <input type="checkbox"/> L-PA <input type="checkbox"/> R-cam mic <input type="checkbox"/> System date and time.		Post production: <input type="checkbox"/> Files synked Edit by: <input type="checkbox"/> Set Scanned Review by: <input type="checkbox"/> Edit - set done <input type="checkbox"/> Review - done		

☐ Sound checkTitle: ☐ Open source RTL verification with Verilator

Presenter(s): Karol Gugala

Start: HH_MM_SS

09:00 (9:00 am)

Length: 00:20:00

End: HH_MM_SS

09:20 (9:20 am)

id: 15370 conf: 15

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit ☐ Review☐ Sound checkTitle: ☐ Sonata: A development platform to enable exploring the use of CHERI for embedded applications

Presenter(s): Hugo McNally

Start: HH_MM_SS

09:20 (9:20 am)

Length: 00:20:00

End: HH_MM_SS

09:40 (9:40 am)

id: 15371 conf: 16

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit ☐ Review☐ Sound checkTitle: ☐ Transparent Checkpointing for Fault Tolerance in RISC-V

Presenter(s): Aayushi Gautam

Start: HH_MM_SS

09:40 (9:40 am)

Length: 00:20:00

End: HH_MM_SS

10:00 (10:00 am)

id: 15372 conf: 17

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit ☐ Review☐ Sound check

Start: _____

Unscheduled

Lighting Talks

Keynote

☐ ☐ ☐ ☐ misc.☐ Added to Veyepar

End: _____

☐ Edit ☐ Review

☐ Equipment problems☐ Notes on back

b45r230

Apr 20 Sat

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☐ Sound check Title: ☐ HDLAgent, Enhancing Hardware Language in the age of LLMs

Presenter(s): Jose Renau

Start: HH_MM_SS

10:20 (10:20 am)

Length: 00:20:00

End: HH_MM_SS

10:40 (10:40 am)

id: 15373 conf: 18

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound check Title: ☐ Spade: An HDL Inspired By Modern Software Languages

Presenter(s): Frans Skarman

Start: HH_MM_SS

10:40 (10:40 am)

Length: 00:20:00

End: HH_MM_SS

11:00 (11:00 am)

id: 15374 conf: 19

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound check Title: ☐ Switchboard: Calling All Hardware Models

Presenter(s): Steven Herbst

Start: HH_MM_SS

11:00 (11:00 am)

Length: 00:20:00

End: HH_MM_SS

11:20 (11:20 am)

id: 15375 conf: 20

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound check Title: ☐ From an Open-Source ISA to Open-Source HW to Open-Source Silicon

Presenter(s): Luca Bertaccini

Start: HH_MM_SS

12:20 (12:20 pm)

Length: 00:20:00

End: HH_MM_SS

12:40 (12:40 pm)

id: 15376 conf: 21

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound check

Start: _____

Lighting Talks

Keynote

☐ ☐ ☐ ☐ misc.☐ Added to Veyepar

End: _____

☐ Edit☐ Review

☐ Equipment problems☐ Notes on back

b45r230

Apr 20 Sat

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☐ Sound checkTitle: ☐ Open Source Hardware: Hacking Silicon for Fun
(instead of profit)

Presenter(s): Troy Benjergedes

Start: HH_MM_SS

12:40 (12:40 pm)

Length: 00:20:00

End: HH_MM_SS

13:00 (1:00 pm)

id: 15377 conf: 22

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ A History of TL-Verilog Google Summer of Code
Projects under FOSSi Foundation

Presenter(s): Steve Hoover

Start: HH_MM_SS

13:00 (1:00 pm)

Length: 00:20:00

End: HH_MM_SS

13:20 (1:20 pm)

id: 15378 conf: 23

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ UMI: Universal Memory Interface
Presenter(s): Andreas OlofssonStart: HH_MM_SS

13:20 (1:20 pm)

Length: 00:20:00

End: HH_MM_SS

13:40 (1:40 pm)

id: 15379 conf: 24

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ ABC: The Way It Should Have Been Designed
Presenter(s): Alan MishchenkoStart: HH_MM_SS

14:20 (2:20 pm)

Length: 00:20:00

End: HH_MM_SS

14:40 (2:40 pm)

id: 15380 conf: 25

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound check

Lighting Talks

Keynote

Start: _____

☐ ☐ ☐ ☐ misc.☐ Added to Veyepar

End: _____

☐ Edit☐ Review

☐ Equipment problems☐ Notes on back

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☐ Sound checkTitle: ☐ BYOL (Build Your Own Linter) – UVMLint for IEEE-UVM
core code development

Presenter(s): Ajeetha Kumari Venkatesan

Start: HH_MM_SS

14:40 (2:40 pm)

Length: 00:20:00

End: HH_MM_SS

15:00 (3:00 pm)

id: 15381 conf: 26

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Beyond EDA lies Edalize
Presenter(s): Olof KindgrenStart: HH_MM_SS

15:00 (3:00 pm)

Length: 00:20:00

End: HH_MM_SS

15:20 (3:20 pm)

id: 15382 conf: 27

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ RF Front-end receiver design for 2.4GH/5GHz WiFi
application

Presenter(s): Jabeom Koo

Start: HH_MM_SS

15:40 (3:40 pm)

Length: 00:20:00

End: HH_MM_SS

16:00 (4:00 pm)

id: 15383 conf: 28

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ CACE Study: Open source analog and mixed-signal
design flow

Presenter(s): Tim Edwards

Start: HH_MM_SS

16:00 (4:00 pm)

Length: 00:20:00

End: HH_MM_SS

16:20 (4:20 pm)

id: 15384 conf: 29

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound check

Start: _____

Lighting Talks

Keynote

☐ ☐ ☐ ☐ misc.☐ Added to Veyepar

End: _____

☐ Edit☐ Review

☐ Equipment problems☐ Notes on back

b45r230

Apr 20 Sat

b45r230

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☐ Sound checkTitle: ☐ IHP Open Source PDK: Announcement, Setup, Current State and Experiences, and look ahead

Presenter(s): Frank Vater

Start:

16:20 (4:20 pm)

Length: 00:20:00

End:

16:40 (4:40 pm)

id: 15385 conf: 30

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Tiny Tapeout: custom silicon open to all
Presenter(s): Pat DeeganStart:

16:40 (4:40 pm)

Length: 00:20:00

End:

17:00 (5:00 pm)

id: 15386 conf: 31

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkStart:

Unscheduled

Lighting Talks

Keynote

End: ☐ ☐ ☐ ☐ misc.☐ Added to Veyepar☐ Edit☐ Review