

Fossi Foundation: Latch_2024

Recording Sheet

☐ Equipment problems

☐ Notes on back

vencaf

Apr 19 Fri

VenCaf

set page 1 of 8

Kit No:	Director:	Cam Op:	Audio op:	File count: _____
Pre-Day check: <input type="checkbox"/> Replace Batteries in mics Sound levels: <input type="checkbox"/> L-PA <input type="checkbox"/> R-cam mic <input type="checkbox"/> System date and time.		Post production: <input type="checkbox"/> Files synked Edit by: <input type="checkbox"/> Set Scanned Review by: <input type="checkbox"/> Edit - set done <input type="checkbox"/> Review - done		

☐ Sound check

Title: ☐ Application of AI-computer vision based software

Presenter(s): Max Faramarzi

Start: HH_MM_SS

18:00 (6:00 pm)

canceled

Length: 00:3:00

End: HH_MM_SS

18:03 (6:03 pm)

id: 15387 conf: 32

Presenter email:

☐ ☐ ☐ ☐ misc.

☐ Edit

☐ Review

☐ Sound check

Title: ☐ CitizenSilicon: towards an open-source Czochralski furnace for Si growth

Presenter(s): Max Aalto

Start: HH_MM_SS

18:00 (6:00 pm)

canceled

Length: 00:3:00

End: HH_MM_SS

18:03 (6:03 pm)

id: 15393 conf: 38

Presenter email:

☐ ☐ ☐ ☐ misc.

☐ Edit

☐ Review

☐ Sound check

Title: ☐ Building an Ecosystem for Hardware Generators

Presenter(s): Rachit Nigam

Start: HH_MM_SS

18:00 (6:00 pm)

canceled

Length: 00:3:00

End: HH_MM_SS

18:03 (6:03 pm)

id: 15392 conf: 37

Presenter email:

☐ ☐ ☐ ☐ misc.

☐ Edit

☐ Review

☐ Sound check

Unscheduled

Start: _____

Lighting Talks

Keynote

☐ ☐ ☐ ☐ misc.

☐ Added to Veyepar

End: _____

☐ Edit

☐ Review

Latch_2024

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vencaf

Apr 19 Fri

VenCaf

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☐ Sound checkTitle: ☐ Tim's Silicon Presentations

Presenter(s): Tim Ansell

Start: HH_MM_SS

18:14 (6:14 pm)

Length: 00:3:00

End: HH_MM_SS

18:17 (6:17 pm)

id: 15394 conf: 100

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Surfer - An Extensible and Snappy Waveform Viewer

Presenter(s): Frans Skarman

Start: HH_MM_SS

18:18 (6:18 pm)

Length: 00:3:00

End: HH_MM_SS

18:21 (6:21 pm)

id: 15395 conf: 101

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Clean up your EDA flows with tclint

Presenter(s): Noah Moroze

Start: HH_MM_SS

18:21 (6:21 pm)

Length: 00:3:00

End: HH_MM_SS

18:24 (6:24 pm)

id: 15391 conf: 36

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Attempts to design hardware using dafny

Presenter(s): Ben Reynwar

Start: HH_MM_SS

18:24 (6:24 pm)

Length: 00:3:00

End: HH_MM_SS

18:27 (6:27 pm)

id: 15388 conf: 33

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound check

Start: _____

Lighting Talks

Keynote

☐ ☐ ☐ ☐ misc.☐ Added to Veyepar

End: _____

☐ Edit☐ Review

☐ Equipment problems☐ Notes on back

vencaf

Apr 19 Fri

VenCaf

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☐ Sound checkTitle: ☐ Zeno: An Open-Source Scalable Capability-Based Secure Architecture

Presenter(s): Alan Ehret

Start: HH_MM_SS

18:27 (6:27 pm)

Length: 00:3:00

End: HH_MM_SS

18:30 (6:30 pm)

id: 15396 conf: 102

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Accelerating Hardware Design with Custom GPTs
Presenter(s): Prakash Shvetank

Start: HH_MM_SS

18:29 (6:29 pm)

Length: 00:3:00

End: HH_MM_SS

18:32 (6:32 pm)

id: 15389 conf: 34

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ OSHHISS Open Source for Hybrid Hetrogenous Integrated Semiconductor Systems

Presenter(s): John Goodenough

Start: HH_MM_SS

18:32 (6:32 pm)

Length: 00:3:00

End: HH_MM_SS

18:35 (6:35 pm)

id: 15390 conf: 35

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Where Community Powers Innovation
Presenter(s): Mohamed Kassem

Start: HH_MM_SS

18:35 (6:35 pm)

Length: 00:3:00

End: HH_MM_SS

18:38 (6:38 pm)

id: 15397 conf: 103

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound check

Start: _____

Lighting Talks

Keynote

☐ ☐ ☐ ☐ misc.☐ Added to Veyepar

End: _____

☐ Edit☐ Review

Fossi Foundation: Latch_2024**Recording Sheet**☐ Equipment problems☐ Notes on back

vencaf

Apr 19 Fri

VenCaf

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☐ Sound checkTitle: ☐ CoreScore like never before

Presenter(s): Olof Kindgren

Start: HH_MM_SS

18:40 (6:40 pm)

Length: 00:3:00

End: HH_MM_SS

18:43 (6:43 pm)

id: 15398 conf: 104

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ ChipWhisperer: Past & Future of a FPGA-based Research Tool

Presenter(s): Jean-Pierre Thibault

Start: HH_MM_SS

18:43 (6:43 pm)

Length: 00:3:00

End: HH_MM_SS

18:46 (6:46 pm)

id: 15399 conf: 105

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ clock

Presenter(s): ck

Start: HH_MM_SS

19:07 (7:07 pm)

Length: 00:00:20

End: HH_MM_SS

19:08 (7:08 pm)

id: 15400 conf:

Released: ☐ yes ☐ no.

Presenter email:

Presenter initials for intent to release: _____

☐ None ☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound check

Start: _____

Lighting Talks

Keynote

☐ ☐ ☐ ☐ misc.☐ Added to Veyepar

End: _____

Released: ☐ yes ☐ no.

Presenter initials for intent to release: _____

☐ Edit☐ Review

Latch_2024

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b45r230

Apr 19 Fri

b45r230

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Kit No:	Director:	Cam Op:	Audio op:	File count: _____
Pre-Day check: <input type="checkbox"/> Replace Batteries in mics Sound levels: <input type="checkbox"/> L-PA <input type="checkbox"/> R-cam mic <input type="checkbox"/> System date and time.		Post production: <input type="checkbox"/> Files synked Edit by: <input type="checkbox"/> Set Scanned Review by: <input type="checkbox"/> Edit - set done <input type="checkbox"/> Review - done		

☐ Sound check
 Title: ☐ Caster: An Open-source E-Ink Controller
 Presenter(s): Wenting Zhang

Start: HH_MM_SS

09:20 (9:20 am)

Length: 00:20:00

End: HH_MM_SS

09:40 (9:40 am)

id: 15356 conf: 1

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit ☐ Review☐ Sound check
 Title: ☐ Teaching Modern EDA using a Tapeout-Centric
 University Course

Presenter(s): Anish Singhani

Start: HH_MM_SS

09:40 (9:40 am)

Length: 00:20:00

End: HH_MM_SS

10:00 (10:00 am)

id: 15357 conf: 2

Presenter email:

☐ None ☐ ☐ ☐ ☐ misc.☐ Edit ☐ Review☐ Sound check
 Title: ☐ CedarEDA for open source silicon
 Presenter(s): Keno Fischer

Start: HH_MM_SS

10:20 (10:20 am)

Length: 00:20:00

End: HH_MM_SS

10:40 (10:40 am)

id: 15358 conf: 3

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit ☐ Review☐ Sound check

Start: _____

Unscheduled

Lighting Talks

Keynote

☐ ☐ ☐ ☐ misc.☐ Added to Veyepar

End: _____

☐ Edit ☐ Review

☐ Equipment problems☐ Notes on back

b45r230

Apr 19 Fri

b45r230

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☐ Sound checkTitle: ☐ Cohort: Software-Oriented Acceleration for You, Me,
and Our Heterogeneous SoCs

Presenter(s): Nazerke Turtayeva

Start: HH_MM_SS

10:40 (10:40 am)

Length: 00:20:00

End: HH_MM_SS

11:00 (11:00 am)

id: 15359 conf: 4

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Towards Cycle-accurate Simulation of xBGAS

Presenter(s): Jie Li

Start: HH_MM_SS

10:53 (10:53 am)

Length: 00:20:00

End: HH_MM_SS

11:13 (11:13 am)

id: 15361 conf: 6

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Towards xBGAS on CHERI: Examining the Benefits of a
Secure Distributed Architecture

Presenter(s): Mert Side

Start: HH_MM_SS

11:00 (11:00 am)

Length: 00:20:00

End: HH_MM_SS

11:20 (11:20 am)

id: 15360 conf: 5

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Artifact Evaluation for the Field Programmable Gate
Array Community

Presenter(s): Miriam Leeser

Start: HH_MM_SS

12:40 (12:40 pm)

Length: 00:20:00

End: HH_MM_SS

13:00 (1:00 pm)

id: 15362 conf: 7

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound check

Lighting Talks

Keynote

Start: _____

☐ ☐ ☐ ☐ misc.☐ Added to Veyepar

End: _____

☐ Edit☐ Review

☐ Equipment problems☐ Notes on back

b45r230

Apr 19 Fri

b45r230

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☐ Sound checkTitle: ☐ Chisel 6 and beyond

Presenter(s): Jack Koenig

Start: HH_MM_SS

13:00 (1:00 pm)

Length: 00:20:00

End: HH_MM_SS

13:20 (1:20 pm)

id: 15363 conf: 8

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ MRPHS: Enabling Transaction-level Deductive Formal
Verification Through PDVL

Presenter(s): Tobias Strauch

Start: HH_MM_SS

13:20 (1:20 pm)

Length: 00:20:00

End: HH_MM_SS

13:40 (1:40 pm)

id: 15364 conf: 9

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Architecture 2.0: Toward Open Source Foundation
Models and Datasets for Hardware Design

Presenter(s): Vijay Janapa Reddi, Shvetank Prakash

Start: HH_MM_SS

14:20 (2:20 pm)

Length: 00:20:00

End: HH_MM_SS

14:40 (2:40 pm)

id: 15365 conf: 10

Presenter email:

☐ None ☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Riding The Wave: Building Wave Pipelines in FPGAs

Presenter(s): Rice Shelley

Start: HH_MM_SS

14:40 (2:40 pm)

Length: 00:20:00

End: HH_MM_SS

15:00 (3:00 pm)

id: 15366 conf: 11

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound check

Lighting Talks

Keynote

Start: _____

☐ ☐ ☐ ☐ misc.☐ Added to Veyepar

End: _____

☐ Edit☐ Review

☐ Equipment problems☐ Notes on back

b45r230

Apr 19 Fri

b45r230

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☐ Sound checkTitle: ☐ Giving Students A Byte of Open-Source: Advancing Hardware Education

Presenter(s): Ethan Sifferman

Start: HH_MM_SS

15:00 (3:00 pm)

Length: 00:20:00

End: HH_MM_SS

15:20 (3:20 pm)

id: 15367 conf: 12

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ Open-source resources for learning the Bluespec HL-HDLs

Presenter(s): Rishiyur Nikhil

Start: HH_MM_SS

15:40 (3:40 pm)

Length: 00:20:00

End: HH_MM_SS

16:00 (4:00 pm)

id: 15368 conf: 13

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound checkTitle: ☐ PyHDL-IF: An Easy-to-Use Python/HDL Cross-Calling Interface

Presenter(s): Matt Ballance

Start: HH_MM_SS

16:00 (4:00 pm)

Length: 00:20:00

End: HH_MM_SS

16:20 (4:20 pm)

id: 15369 conf: 14

Presenter email:

☐ ☐ ☐ ☐ misc.☐ Edit☐ Review☐ Sound check

Start: _____

Unscheduled

Lighting Talks

Keynote

End: _____

☐ ☐ ☐ ☐ misc.☐ Added to Veyepar☐ Edit☐ Review