

# Recording Release Agreement

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**Name of Presenter:**

**Name of Presentation:** Welcome

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Talk ID: 14798 0  
**Room:** Revolution Hall  
**Talk Start:** May 04 Sat 09:00  
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**Name of Presenter:**

**Name of Presentation:** FOSSi Foundation Update

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**Name of Presenter:** Aliaksei Chapyzenka

**Name of Presentation:** Diagrams and system visualisation in chip de

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**Name of Presenter:** Neil Turley

**Name of Presentation:** Netlistsvg: How to Draw a Better Schematic tl

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**Name of Presenter:** Julius Baxter

**Name of Presentation:** Lessons learned customising the Rocket RIS!

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**Name of Presenter:** Jack Koenig

**Name of Presentation:** Higher-Order Hardware Design with Chisel 3

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**Name of Presenter:** Christopher Felton

**Name of Presentation:** The fusion of high-level synthesis with event-

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**Name of Presenter:** Katie Lim

**Name of Presentation:** JuxtaPiton: The First Open-Source, Heteroge

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**Name of Presenter:** Jonathan Balkind

**Name of Presentation:** OpenPiton+Ariane: Making Ariane Multicore \

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**Name of Presenter:** Joshua Wise

**Name of Presentation:** Lessons Learned from Open-Sourcing NVDL

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**Name of Presenter:** Matthew Ballance

**Name of Presentation:** DVKit: An Integrated Development Environment

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**Name of Presenter:** Jose Renau

**Name of Presentation:** Live Graph infrastructure for Synthesis and S

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**Name of Presenter:** Jerry Zhao, Abe Gonzalez, Ben Korpan

**Name of Presentation:** The Berkeley-Out-of-Order-Machine: An Ope

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**Name of Presenter:** David Biancolin, Alon Amid

**Name of Presentation:** FireSim: Open-Source Easy-to-use FPGA-Ac

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**Name of Presenter:** Wenting Zhang

**Name of Presentation:** How I started learning FPGA: My journey writ

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**Name of Presenter:** Cole Johnson

**Name of Presentation:** Emulation of vintage integrated circuits through

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**Name of Presenter:**

**Name of Presentation:** Mingle, drinks

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**Name of Presenter:** Daniel Petrisko

**Name of Presentation:** BaseJump STL: a Standard Template Library

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**Name of Presenter:** Jim Lewis

**Name of Presentation:** OSVVM, VHDL's #1 FPGA Verification Librar

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**Name of Presenter:** Matthew Ballance

**Name of Presentation:** Verifying Open-Source Silicon with SystemVe

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**Name of Presenter:** Vagrant Cascadian

**Name of Presentation:** RISC-V in Debian

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**Name of Presenter:**

**Name of Presentation:** Lightning Talks

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**Name of Presenter:** Steve Haynal

**Name of Presentation:** Hermes-Lite: Amateur Radio SDR

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**Name of Presenter:** Tim 'mithro' Ansell

**Name of Presentation:** Tim has too many projects - LatchUp Edition

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**Name of Presenter:** Olof Kindgren

**Name of Presentation:** FuseSoC - Cores have never been so much f

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**Name of Presenter:** Aliaksei Chapyzenka

**Name of Presentation:** DUH: document and tools for HW design reus

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**Name of Presenter:** Jeff Bush

**Name of Presentation:** Nyuzi: An Open Source GPGPU Processor

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Talk ID: 14822 24  
**Room:** Revolution Hall  
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**Name of Presenter:** Matthew Guthaus

**Name of Presentation:** OpenRAM: An Open Source Memory Compiler

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**Name of Presenter:** Tom McLeod

**Name of Presentation:** SYZYGY: An Open Standard For Semicondu

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**Name of Presenter:** Piotr Esden-Tempski

**Name of Presentation:** Open-Source FPGA tools, how and why?

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