

Presenter(s):

Talk ID:
14798
0

Talk Title: Welcome

5 min pre: 8:55 am **Talk Start:** 9:00 am **Length:** 0:10:0 **End -5m:** 9:05 am **End:** 9:10 am

- [] Have a water bottle for ready for the presenter
- [] Ensure presenter has microphone and completed sound check (if not, poke AV team)
- [] Check that HDMI is plugged into the presenter's laptop
- [] Ensure presenter's display appears on the projector and confidence monitors
- [] Ask the presenter "Will there be laptop sound?" If so, ask AV to plug in and test sound
- [] Ask the presenter how they would like to be introduced and how to pronounce their name
- [] Ask the presenter if they would like time warnings, and if so, when they would like them

- [] When the presenter is ready, un-mute the microphone
- [] Make any special announcements (eg. Audience defrag, events later in the day)
- [] Hit the Big Red Button. (On = recording starts)
- [] Introduce the presenter(s) and the talk

Presenter(s):

Talk ID:
14799
1

Talk Title: FOSSi Foundation Update

5 min pre: 9:05 am **Talk Start:** 9:10 am **Length:** 0:20:0 **End -5m:** 9:25 am **End:** 9:30 am

- [] Have a water bottle for ready for the presenter
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Presenter(s): Aliaksei Chapyzhenka

Talk ID:
14800
2

Talk Title: Diagrams and system visualisation in chip design

5 min pre: 9:25 am **Talk Start:** 9:30 am **Length:** 0:30:0 **End -5m:** 9:55 am **End:** 10:00 am

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- [] Introduce the presenter(s) and the talk

Presenter(s): Neil Turley

Talk ID:
14801
3

Talk Title: Netlistsvg: How to Draw a Better Schematic than Graphviz

5 min pre: 9:55 am **Talk Start:** 10:00 am **Length:** 0:30:0 **End -5m:** 10:25 am **End:** 10:30 am

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- [] Introduce the presenter(s) and the talk

Presenter(s): Julius Baxter

Talk ID:
14802
4

Talk Title: Lessons learned customising the Rocket RISC-V core

5 min pre: 10:45 am **Talk Start:** 10:50 am **Length:** 0:20:0 **End -5m:** 11:05 am **End:** 11:10 am

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 - Introduce the presenter(s) and the talk

Presenter(s): Jack Koenig

Talk ID:
14803
5

Talk Title: Higher-Order Hardware Design with Chisel 3

5 min pre: 11:05 am **Talk Start:** 11:10 am **Length:** 0:30:0 **End -5m:** 11:35 am **End:** 11:40 am

- Have a water bottle for ready for the presenter
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 - Make any special announcements (eg. Audience defrag, events later in the day)
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 - Introduce the presenter(s) and the talk

Presenter(s): Christopher Felton

Talk ID:
14804

Talk Title: The fusion of high-level synthesis with event-oriented hardware description (myhdl)

6

5 min pre: 11:35 am **Talk Start:** 11:40 am **Length:** 0:30:0 **End -5m:** 12:05 pm **End:** 12:10 pm

- [] Have a water bottle for ready for the presenter
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 - [] Introduce the presenter(s) and the talk

Presenter(s): Katie Lim

Talk ID:
14805

Talk Title: JuxtaPiton: The First Open-Source, Heterogeneous-ISA Processor

7

5 min pre: 1:25 pm **Talk Start:** 1:30 pm **Length:** 0:20:0 **End -5m:** 1:45 pm **End:** 1:50 pm

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 - [] Introduce the presenter(s) and the talk

Presenter(s): Jonathan Balkind

Talk ID:
14806
8

Talk Title: OpenPiton+Ariane: Making Ariane Multicore with OpenPiton's P-Mesh

5 min pre: 1:45 pm **Talk Start:** 1:50 pm **Length:** 0:20:0 **End -5m:** 2:05 pm **End:** 2:10 pm

-] Have a water bottle for ready for the presenter
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 -] Introduce the presenter(s) and the talk

Presenter(s): Joshua Wise

Talk ID:
14807
9

Talk Title: Lessons Learned from Open-Sourcing NVDLA

5 min pre: 2:05 pm **Talk Start:** 2:10 pm **Length:** 0:20:0 **End -5m:** 2:25 pm **End:** 2:30 pm

-] Have a water bottle for ready for the presenter
 -] Ensure presenter has microphone and completed sound check (if not, poke AV team)
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 -] Introduce the presenter(s) and the talk

Presenter(s): Matthew Ballance

Talk ID:
14808

Talk Title: DVKit: An Integrated Development Environment for Design and Verification Engineers

10

5 min pre: 2:25 pm **Talk Start:** 2:30 pm **Length:** 0:20:0 **End -5m:** 2:45 pm **End:** 2:50 pm

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 - [] Hit the Big Red Button. (On = recording starts)
 - [] Introduce the presenter(s) and the talk

Presenter(s): Jose Renau

Talk ID:
14809

Talk Title: Live Graph infrastructure for Synthesis and Simulation

11

5 min pre: 3:25 pm **Talk Start:** 3:30 pm **Length:** 0:30:0 **End -5m:** 3:55 pm **End:** 4:00 pm

- [] Have a water bottle for ready for the presenter
 - [] Ensure presenter has microphone and completed sound check (if not, poke AV team)
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-
- [] When the presenter is ready, un-mute the microphone
 - [] Make any special announcements (eg. Audience defrag, events later in the day)
 - [] Hit the Big Red Button. (On = recording starts)
 - [] Introduce the presenter(s) and the talk

Presenter(s): Jerry Zhao, Abe Gonzalez, Ben Korpan

Talk ID:
14810

Talk Title: The Berkeley-Out-of-Order-Machine: An Open Source Synthesizable High-Performance RISC-V Processor

5 min pre: 3:55 pm **Talk Start:** 4:00 pm **Length:** 0:30:0 **End -5m:** 4:25 pm **End:** 4:30 pm

- [] Have a water bottle for ready for the presenter
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- [] Hit the Big Red Button. (On = recording starts)
- [] Introduce the presenter(s) and the talk

Presenter(s): David Biancolin, Alon Amid

Talk ID:
14811

Talk Title: FireSim: Open-Source Easy-to-use FPGA-Accelerated Cycle-Exact Hardware Simulation in the Cloud

5 min pre: 4:25 pm **Talk Start:** 4:30 pm **Length:** 0:30:0 **End -5m:** 4:55 pm **End:** 5:00 pm

- [] Have a water bottle for ready for the presenter
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- [] Introduce the presenter(s) and the talk

Presenter(s): Wenting Zhang

Talk ID:
14812
14

Talk Title: How I started learning FPGA: My journey writing a GameBoy in Verilog

5 min pre: 5:25 pm **Talk Start:** 5:30 pm **Length:** 0:30:0 **End -5m:** 5:55 pm **End:** 6:00 pm

- [] Have a water bottle for ready for the presenter
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 - [] Introduce the presenter(s) and the talk

Presenter(s): Cole Johnson

Talk ID:
14813
15

Talk Title: Emulation of vintage integrated circuits through die analysis and reverse-EDA

5 min pre: 5:55 pm **Talk Start:** 6:00 pm **Length:** 0:30:0 **End -5m:** 6:25 pm **End:** 6:30 pm

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 - [] Introduce the presenter(s) and the talk

Presenter(s):

Talk ID:
14814
16

Talk Title: Mingle, drinks

5 min pre: 6:15 pm **Talk Start:** 6:20 pm **Length:** 0:30:0 **End -5m:** 6:45 pm **End:** 6:50 pm

- [] Have a water bottle for ready for the presenter
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- [] Introduce the presenter(s) and the talk

Presenter(s): Daniel Petrisko

Talk ID:
14815

Talk Title: BaseJump STL: a Standard Template Library for Hardware Design

17

5 min pre: 9:25 am **Talk Start:** 9:30 am **Length:** 0:30:0 **End -5m:** 9:55 am **End:** 10:00 am

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Presenter(s): Jim Lewis

Talk ID:
14816

Talk Title: OSVVM, VHDL's #1 FPGA Verification Library

18

5 min pre: 9:55 am **Talk Start:** 10:00 am **Length:** 0:30:0 **End -5m:** 10:25 am **End:** 10:30 am

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Presenter(s): Matthew Ballance

Talk ID:
14817

Talk Title: Verifying Open-Source Silicon with SystemVerilog: Getting in on the Ground Floor

19

5 min pre: 10:25 am **Talk Start:** 10:30 am **Length:** 0:30:0 **End -5m:** 10:55 am **End:** 11:00 am

- Have a water bottle for ready for the presenter
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Presenter(s): Vagrant Cascadian

Talk ID:
14818

Talk Title: RISC-V in Debian

20

5 min pre: 11:15 am **Talk Start:** 11:20 am **Length:** 0:20:0 **End -5m:** 11:35 am **End:** 11:40 am

- Have a water bottle for ready for the presenter
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Presenter(s):Talk ID:
14819
21**Talk Title:** Lightning Talks**5 min pre:** 11:35 am **Talk Start:** 11:40 am **Length:** 0:30:0 **End -5m:** 12:05 pm **End:** 12:10 pm

- Have a water bottle for ready for the presenter
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 - Introduce the presenter(s) and the talk

Presenter(s): Steve HaynalTalk ID:
14826**Talk Title:** Hermes-Lite: Amateur Radio SDR**5 min pre:** 11:44 am **Talk Start:** 11:49 am **Length:** 00:12:00 **End -5m:** 11:56 am **End:** 12:01 pm

- Have a water bottle for ready for the presenter
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 - Hit the Big Red Button. (On = recording starts)
 - Introduce the presenter(s) and the talk

Presenter(s): Tim 'mithro' Ansell

Talk ID:
14827

Talk Title: Tim has too many projects - LatchUp Edition

5 min pre: 11:56 am **Talk Start:** 12:01 pm **Length:** 00:28:00 **End -5m:** 12:24 pm **End:** 12:29 pm

- [] Have a water bottle for ready for the presenter
 - [] Ensure presenter has microphone and completed sound check (if not, poke AV team)
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 - [] Introduce the presenter(s) and the talk

Presenter(s): Olof Kindgren

Talk ID:
14820

Talk Title: FuseSoC - Cores have never been so much fun

22

5 min pre: 1:15 pm **Talk Start:** 1:20 pm **Length:** 0:30:0 **End -5m:** 1:45 pm **End:** 1:50 pm

- [] Have a water bottle for ready for the presenter
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 - [] Introduce the presenter(s) and the talk

Presenter(s): Aliaksei Chapyzhenka

Talk ID:
14821
23

Talk Title: DUH: document and tools for HW design reuse

5 min pre: 1:45 pm **Talk Start:** 1:50 pm **Length:** 0:30:0 **End -5m:** 2:15 pm **End:** 2:20 pm

- [] Have a water bottle for ready for the presenter
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Presenter(s): Jeff Bush

Talk ID:
14822
24

Talk Title: Nyuzi: An Open Source GPGPU Processor

5 min pre: 2:15 pm **Talk Start:** 2:20 pm **Length:** 0:30:0 **End -5m:** 2:45 pm **End:** 2:50 pm

- [] Have a water bottle for ready for the presenter
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Presenter(s): Matthew Guthaus

Talk ID:

14823

25

Talk Title: OpenRAM: An Open Source Memory Compiler

5 min pre: 3:05 pm **Talk Start:** 3:10 pm **Length:** 0:30:0 **End -5m:** 3:35 pm **End:** 3:40 pm

- Have a water bottle for ready for the presenter
 - Ensure presenter has microphone and completed sound check (if not, poke AV team)
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 - Make any special announcements (eg. Audience defrag, events later in the day)
 - Hit the Big Red Button. (On = recording starts)
 - Introduce the presenter(s) and the talk

Presenter(s): Tom McLeod

Talk ID:

14824

26

Talk Title: SYZGY: An Open Standard For Semiconductor Evaluation

5 min pre: 3:35 pm **Talk Start:** 3:40 pm **Length:** 0:30:0 **End -5m:** 4:05 pm **End:** 4:10 pm

- Have a water bottle for ready for the presenter
 - Ensure presenter has microphone and completed sound check (if not, poke AV team)
 - Check that HDMI is plugged into the presenter's laptop
 - Ensure presenter's display appears on the projector and confidence monitors
 - Ask the presenter "Will there be laptop sound?" If so, ask AV to plug in and test sound
 - Ask the presenter how they would like to be introduced and how to pronounce their name
 - Ask the presenter if they would like time warnings, and if so, when they would like them
-
- When the presenter is ready, un-mute the microphone
 - Make any special announcements (eg. Audience defrag, events later in the day)
 - Hit the Big Red Button. (On = recording starts)
 - Introduce the presenter(s) and the talk

Presenter(s): Piotr Esden-Tempski

Talk ID:
14825

Talk Title: Open-Source FPGA tools, how and why?

27

5 min pre: 3:55 pm **Talk Start:** 4:00 pm **Length:** 0:20:0 **End -5m:** 4:15 pm **End:** 4:20 pm

-] Have a water bottle for ready for the presenter
-] Ensure presenter has microphone and completed sound check (if not, poke AV team)
-] Check that HDMI is plugged into the presenter's laptop
-] Ensure presenter's display appears on the projector and confidence monitors
-] Ask the presenter "Will there be laptop sound?" If so, ask AV to plug in and test sound
-] Ask the presenter how they would like to be introduced and how to pronounce their name
-] Ask the presenter if they would like time warnings, and if so, when they would like them

-] When the presenter is ready, un-mute the microphone
-] Make any special announcements (eg. Audience defrag, events later in the day)
-] Hit the Big Red Button. (On = recording starts)
-] Introduce the presenter(s) and the talk