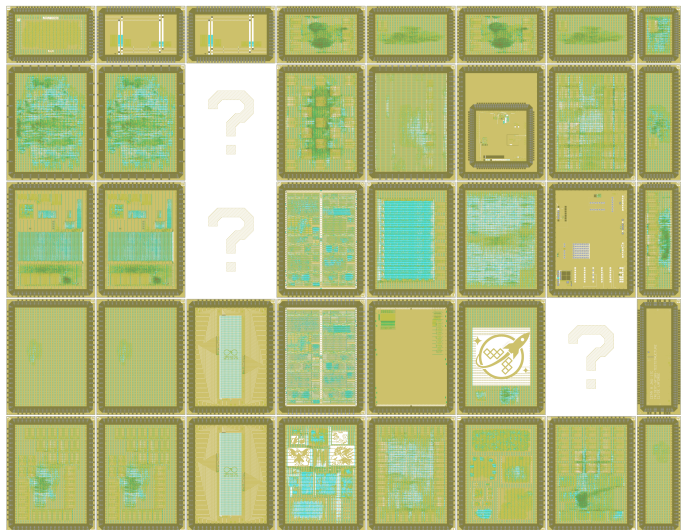


1,000 custom silicon chips for \$2.00 each

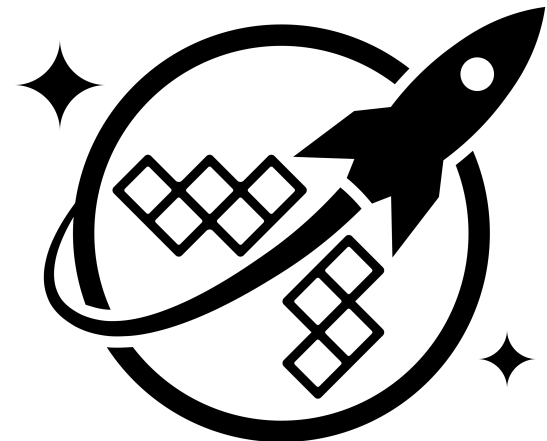
wafer.space



Budget Silicon
Manufacturing



<https://wafer.space>



Submission Deadline for next run

16 December 2026

Interesting Designs:



6502

6502/6510/PLA replicas
SID replicas
Digital-to-Analog converters
<https://github.com/AvalonSemiconductors/ws-submission-2025>

@tholin - WS-Multi



Risc-V SoC

Mike Bell TinyQV - Crowdsourced
Risc-V SoC
<https://github.com/MichaelBell/ws01-tinyQV>

BreakingTTAPs
Transport Triggered
Architecture processor
<https://github.com/polyfractal/BreakingTTAPs>



@lofty Chess-move-generator
<https://github.com/Ravenslofty/gf180mcu-chess>



Z80

Renaldas Zioma

<https://github.com/rejunity/ws0-z80-open-silicon-gf180mcu>

Hiroshi - KianV A 32-bit RISC-V Linux SoC
<https://github.com/splinedrive/gf180mcu-kianv-rv32ima-sv32/>



Leo Moser
Open source FPGA
generated using the
FABulous eFPGA
framework

<https://github.com/mole99/gf180mcu-fabulous-fpga>



Peter Kinget

The MOSbius chip enables
learning about measuring CMOS
circuits and comparing their
operation to analytical design &
analysis

<https://www.crowdsupply.com/wafer-space/gf180mcu-run-1/updates/mosbius>



YOURS!

Get Started.

https://github.com/evezor/wafer_space_docker_based_starter_kit

Full List of WS-1
<https://github.com/wafer-space/ws-run1>

